MAHIDOL UNIVERSITY INTERNATIONAL COLLEGE

Course: ICCS316 Computer Architectures

Text: Computer Organization and Architecture, William Stallings.

Instructor: Asst. Prof. Prachuab Vanitchatchavan, Ph.D.
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Time: Thursday 8:00 AM – 12:00 PM

Evaluation:

Class Attendance	≈	10 %
Class Attention & Participation	≈	10 %
Mid-Term Examination	≈	40 %
Final Examination	≈	40 %

Grading: The passing score for this class is 60%. Any students who have the final score of less than 60% will not pass the course and an "F" will be assigned.

Class Policy: **Class attendance will be strictly monitored**. Any students with the class attendance of less than 80% of the class sessions without reasonable cause(s) will not be permitted to take the final examination. **Arriving after the class started without reasonable explanation** will be considered as "late" (15 minutes) or "absent" (30 minutes). Two "late" will be counted as one "absent".

Tests may be given during class session without prior notification. However, any tests or examinations carry more than 5% will be notified at least one week in advance.

Any academic dishonesty on any assignments (both written and programming) will result in a severe penalty for all parties involved. The term "academic dishonesty" includes copying assignments or programs. The same criteria are applied on all examinations. Penalties for academic dishonesty may range from no grade for that particular work to total failure grade, an "F" for the class.

Objectives: This course is intended to present students with the foundation of computer architectures and aspects of computer hardware systems.

Contents:

Week 1	Chapter 1.	Introduction to the Computer Hardware Systems
Week 2-3	Chapter 2.	Computer Organization
Week 3-5	Chapter 3.	CPU & Instruction Set
Week 6-7	Chapter 4.	Memory Subsystems
Week 8	Chapter 5.	I/O Subsystems
Week 9-11	Chapter 6.	Control Unit